Department of Freshman Engineering

Basic Electrical & Electronics Engineering Lab

Course			20ES	1151	Year			Ι		Sem	Semester		I	
Code			20201131		I cui	Ital			1		Semester		1	
Course			Engineering		Brai	Branch		СЕ		Cou	Course Type		Lab	
Category			Science			Dianon					course Type		Luo	
Credits			1.5		L-T-P			0-0-3		Prer	Prerequisites		Nil	
Continuous			15		Semester End		End	35			Total		50	
Internal					Eval	Evaluation				Mar	Marks			
Evaluation														
Course Outcomes														
Upon su	iccessf	ul com	pletion	n of the	course	e, the st	tudent	will be	able to	С				
CO1	App	pply techniques/procedures of Electrical & Electronics Engineering to solve problems (L3).												
CO2	Cond	onduct experiments as a team / individual by using equipment available in the laboratory.												
CO3	Exa	camine the network theorems and Kirchhoff's laws for DC electrical circuits (L4).												
CO4	Ana	nalyse the open circuit characteristic of DC shunt generator and efficiency of single phase												
		transformer (L4).												
CO5		nalyse the characteristics/ performance parameters of Electronic and Analog Circuits. (L4)												
CO6		ake an effective report based on experiments												
Contribution of Course Outcomes towards achievement of Program Outcomes &														
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0.01	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3			3										
CO2				3	3				3				1	1
CO3		3		3									1	
CO4		3		3									1	
CO5		3		3									1	
CO6				3						3			1	1
							Sylla							
Expt. N	lo.						Syllabı						Mappe	d CO's
									riments	3				
1	V	erifica	ation of	Kirch	hoff's l	Laws k	CVL ar	nd KCI	_ .				CO1,CO2,	
		7 . 0					C1						CO3,CO6	
2	V	erifica	CO1,CO2,											
2	3 Verification of Thevenin's Theorem and Norton's Theorem.							CO3,CO6						
3	V	erifica	CO1,CO2, CO3,CO6											
4)pen ci	CO3,CO6 CO1,CO2,											
4		-	CO1,CO2, CO4,CO6											
5	generator. 5 OC and SC Tests on single phase transformer.								C04,C00					
5									CO1,CO2, CO4,CO6					
6	6 Voltage Current Characteristics of a p-n Junction Diode.								C04,C00 C01,C02,					
0	•	Jugo		C01,C02, C05,C06										
7	F	Ialf wa	ve rect	ifier w	ith and	witho	ut filte	r.					C01,C02,	
,		,, u			1110			- •					C01,C02, C05,C06	
8	F	ull way	ve recti	ifier wi	th and	withou	ıt filter	•					CO1,CO2,	
-													C05,C06	
9	9 Voltage Regulation with Zener Diode.								C01,C02,					

PVP20

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		CO5,CO6					
10	Inverting and Non-inverting Amplifier Design with Op-amp.	CO1,CO2,					
		CO5,CO6					
11	Verification of KCL and KVL using PSPICE.	CO1,CO2,					
		CO3,CO6					
12	Verification of Network Theorems using PSPICE.	CO1,CO2,					
		CO3,CO6					
13	Diode and Transistor Circuit Analysis using PSPICE.	CO1,CO2,					
		CO5,CO6					
14	Inverting and Non-inverting Amplifier Design with Op-ampusing PSPICE.	CO1,CO2,					
		CO5,CO6					
Learning Resources							
Text Books							
1. D.P.Kothari, I.J.Nagrath, Basic Electrical and Electronics Engineering, 1 st Edition, McGraw Hill							
Education (India) Private Limited, 2017.							
2. B.L.Theraja, Fundamentals of Electrical Engineering and Electronics, 1 st Edition, S.Chand Publishing, New Delhi, 2006.							
3. Millman Jacob, Halkias C Christos, Electronic Devices and Circuits, 2 nd Edition, Tata Mcgrawhill Publications, 2007.							
Reference Books							
1. S.K. Bhattacharya, Basic Electrical and Electronics Engineering, Pearson Education, 2011.							
2.	Dharma Raj Cheruku, B T Krishna, Electronic Devices and Circuits, 2 nd Edition, Pearson						
	Education, 2008.						
3.	3. R.K.Rajput, Basic Electrical and Electronics Engineering, University Science Press, New Delhi, 2012.						
e- Resources & other digital material							
1.							
2.	2. https://nptel.ac.in/courses/108105112/						